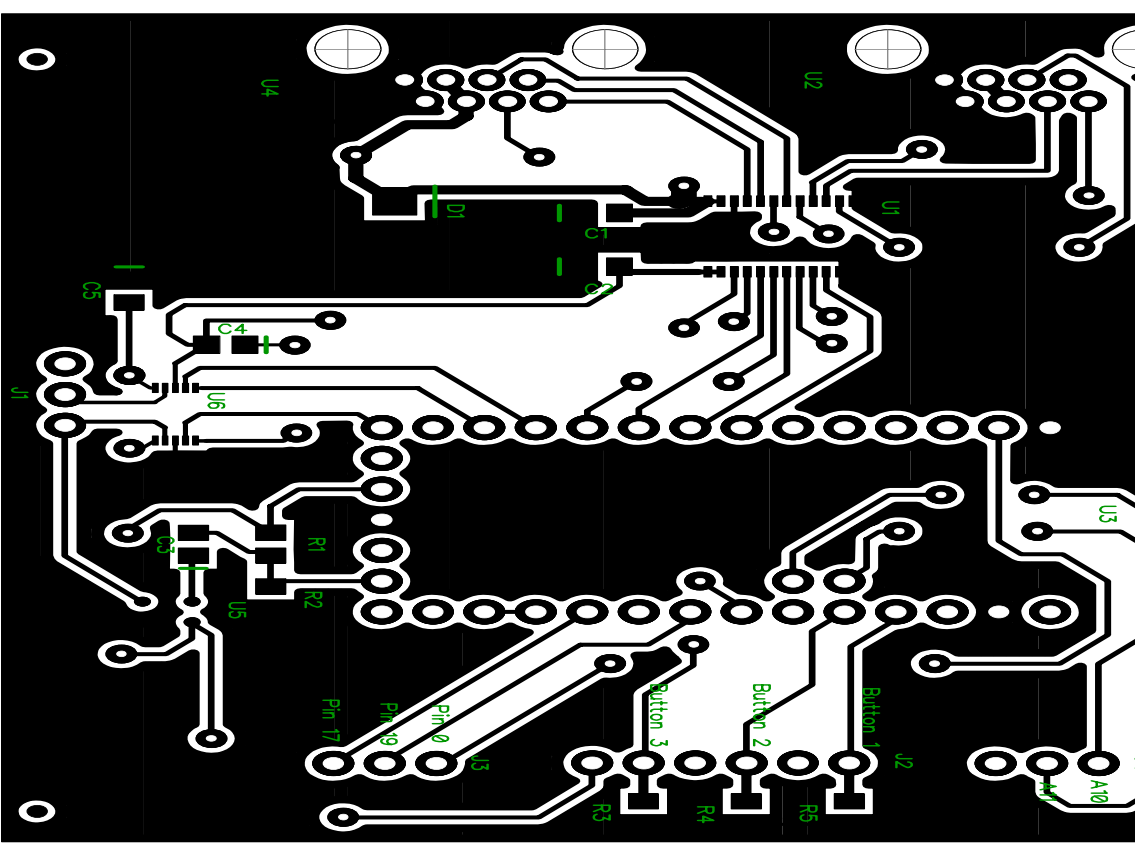
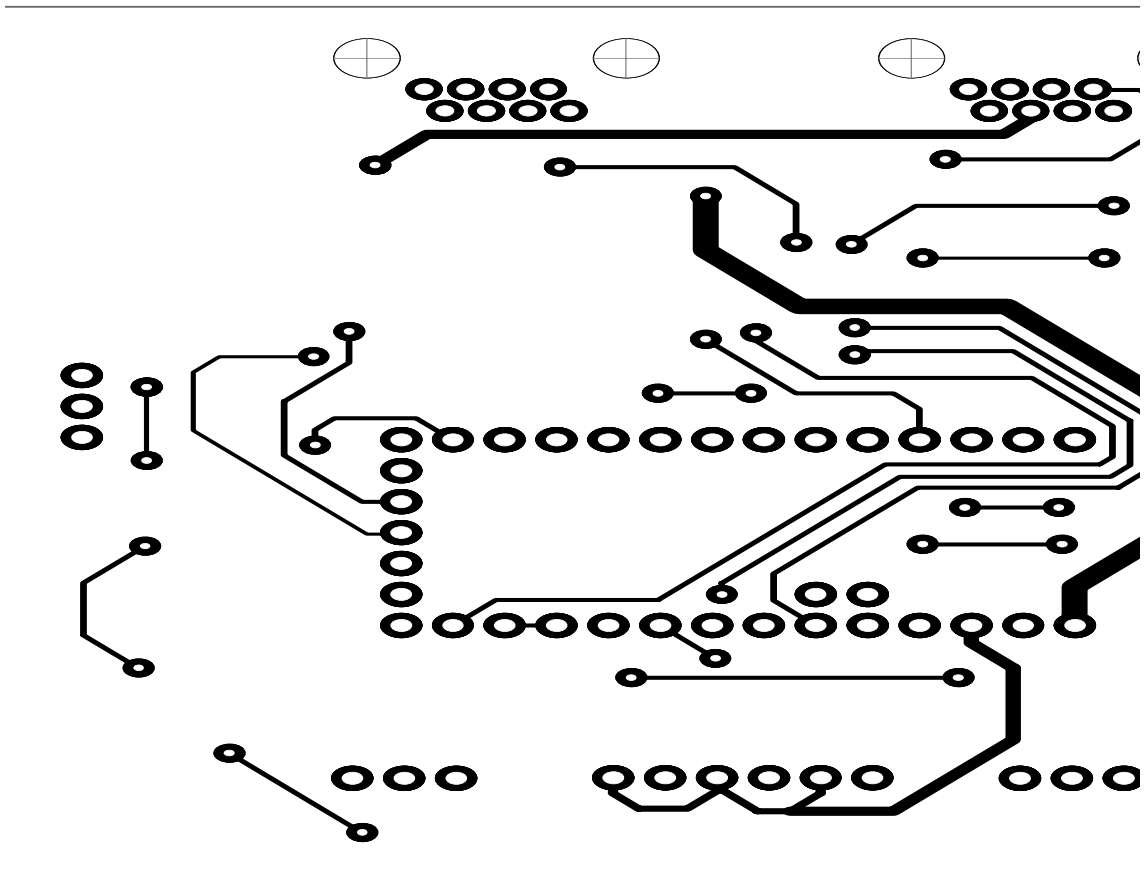


Layout of PCB



Top



Bottom

