

VERONICA CHOW

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EDUCATION

California Polytechnic State University, San Luis Obispo (Cal Poly)

Bachelor of Science in Electrical Engineering

December 20xx

Major GPA: 3.5

Relevant Coursework: Electric Circuit Analysis I-III, Energy Conversion Electro-Magnetics, Digital Design, Continuous Time Signals & Systems, Digital Electronics & Integrated Circuits, Technical Writing for Engineers

TECHNICAL SKILLS

Applications: LabVIEW, OrCAD Capture, LTspice, Adobe InDesign, Microsoft Excel

Programming: Matlab, C, Java, VHDL, Verilog

Hardware: Arduino, data acquisition, electronic test & measurement, spectrum analyzer, FPGA, interferometry, sensor calibration, soldering

INTERNSHIP EXPERIENCE

Test Engineering Intern, Texas Instruments Inc.

Summers 20xx & 20xx

- Validated final test hardware for production use by conducting Gauge Repeatability and Reproducibility (GRR) study
- Collected data on the ETS-364 test system, debugged test program, analyzed data and generated a validation report
- Generated control map to automate probe level GRR data collection
- Designed a PCB to assist with the qualification of relay components over temperature for ATE applications
- Schematic captured test bench board design using Cadence and presented design to test engineering team

ELECTRICAL ENGINEERING PROJECTS

Speech Recognized Writing Robot (Senior Project)

September 20xx – March 20xx

- Collaborated in a team of 5 to design, build, and test a speech recognized writing robot
- Built a speech recognition library using algorithm segmentation
- Built the robotic arm using servomotors and calculated the angles for the alphabet and fed those values to the servomotors
- Enhanced communication skills by helping write 70+ page technical report
- Dedicated 30 hours per week to this team project

Ultrasonic Wireless Lux Meter

April – June 20xx

- Constructed system to measure illuminance, transmit information using a 40 kHz ultrasonic link, then decode, process, and display measurement
- Designed photo current-to-frequency converter, driver amplifier, VCO, band pass amplifier, linear regulator, PLL-based tone decoder, and microcontroller processing components

Inverse Filter for Image Restoration

January – March 20xx

- Designed an inverse filter for restoring a blurred image using Matlab
- Performed mathematical model for the inverse filter

CAMPUS INVOLVEMENT & COMMUNITY SERVICE

Vice-President, Power Engineering Society – Cal Poly

Sept. 20xx – June 20xx

Member, Society of Women Engineers – Cal Poly

Sept. 20xx – Present

Volunteer, GRID Alternatives

Spring 20xx

- Helped install solar power grid panel for a local low-income homeowner

OTHER WORK EXPERIENCE

Tutor, Electrical Engineering Department – Cal Poly (8 hours/week)

September 20xx – June 20xx